

For: Methods And Systems For Forming Embedded DRAM For An MIM Capacitor

Attorney Doc. No.: 67,200-547

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MARK-UP COPY

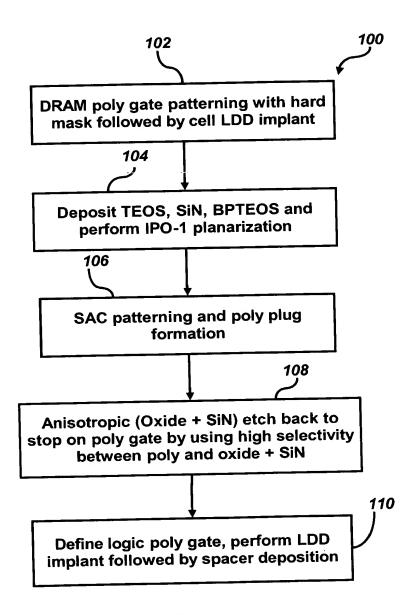


FIG. 16



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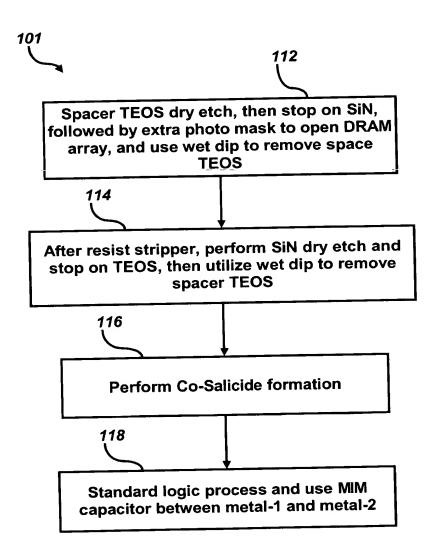


FIG. 17

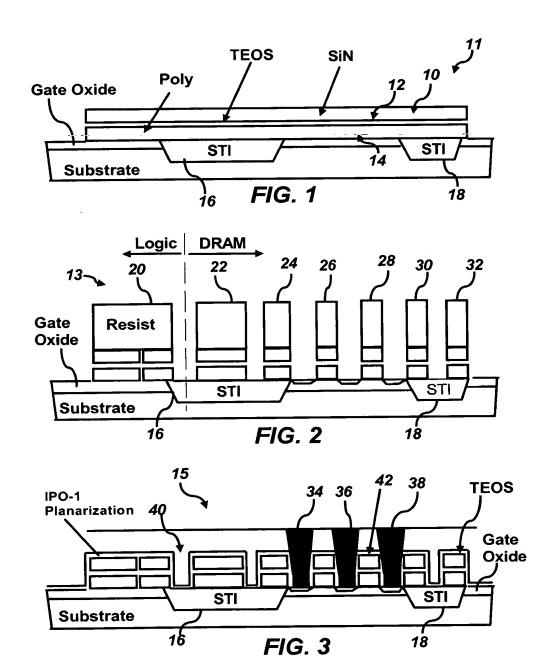


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## REPLACEMENT SHEET

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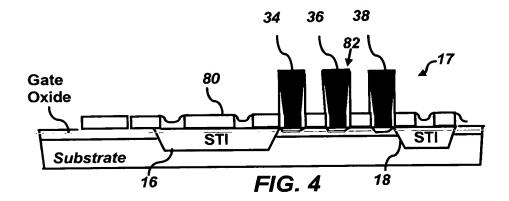


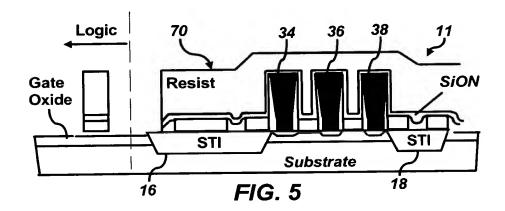


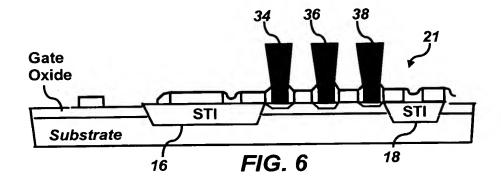
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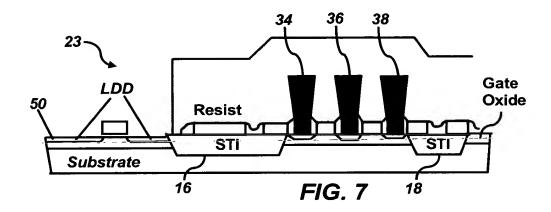


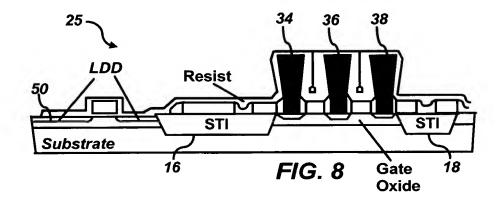


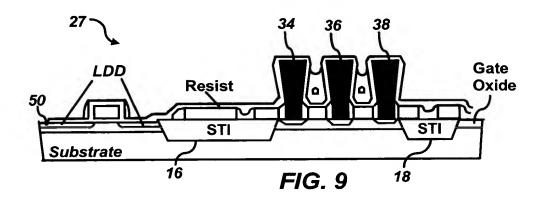
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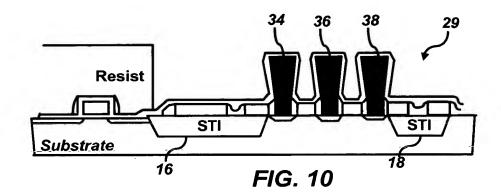


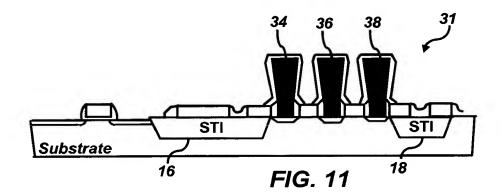


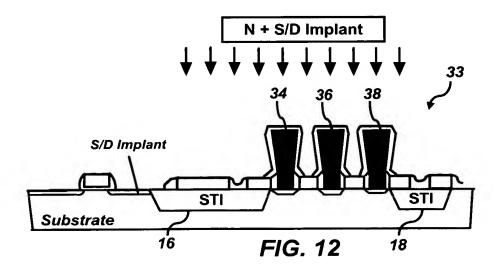
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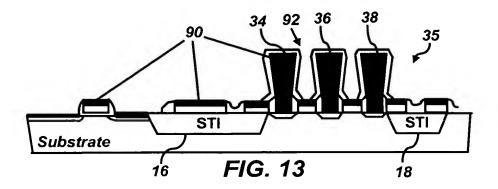


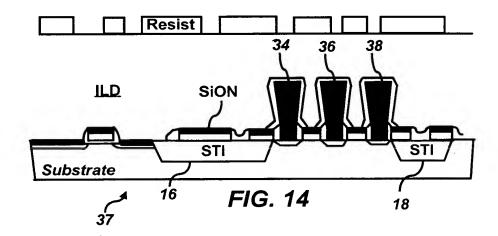


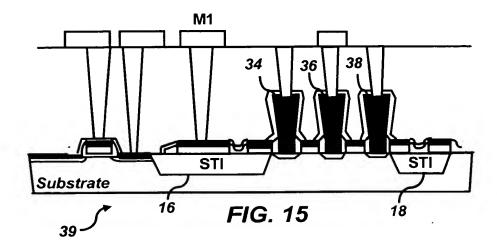


Inventor: Min-Hsiung Chiang, et al Serial No.: 09/975,840 Filed: 10/11/2001 For: Methods And Systems For Forming Embedded DRAM For An MIM Capacitor Attorney Doc. No.: 67,200-547

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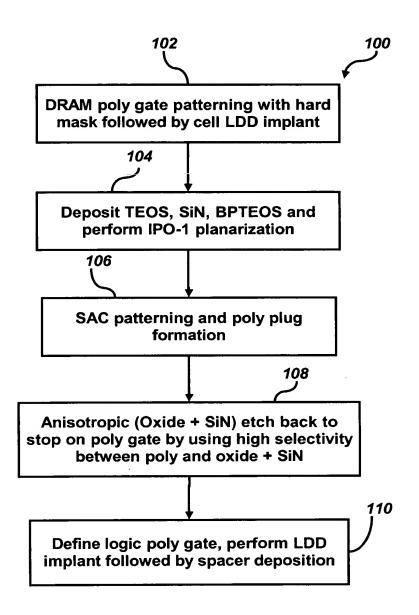


FIG. 16

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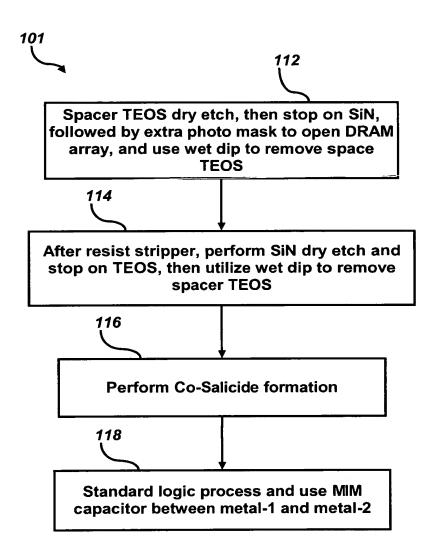


FIG. 17